

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2811
Examiner: Nitin Parekh
Confirmation No.: 8265

In re PATENT APPLICATION of:

Applicant(s): Yoshinori SHIZUNO

Serial No.: 10/697,015

Filed: October 31, 2003

For: Semiconductor device with improved
design freedom of external terminal

Docket No.: OHG 140

**SUBMISSION OF
FORMAL DRAWINGS**

May 17, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby submits 1 new sheet of Formal Drawings (Figs. 1C-D).

Respectfully submitted,

May 17, 2005
Date

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FIG. 1(C)

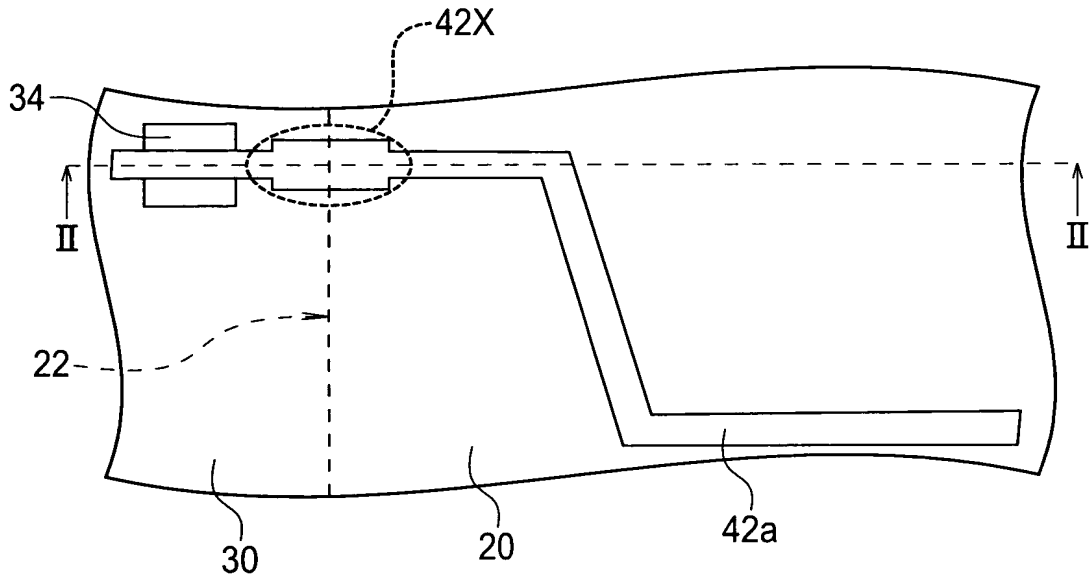


FIG. 1(D)

